



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application of :
Hiroyuki HARADA :
Serial No. 09/872,798 : Group Art Unit - 2814
Filed: June 1, 2001 : Examiner - Thao X. Le
For: VERTICAL MOS TRANSISTOR :
AND A METHOD OF :
MANUFACTURING THE SAME : Docket No. S004-4310

COMMISSIONER OF PATENTS AND TRADEMARKS
Washington, DC 20231

**PETITION FOR EXTENSION OF TIME
AND REQUIRED EXTENSION FEE**

S I R:

Applicant respectfully requests a two-month extension of time for replying to the Office Action dated April 22, 2002, and requests that the period for taking further action be reset to expire on September 22, 2002. This is an extension request for replying to the April 22 Office Action within the second month pursuant to 37 CFR §1.136(a).

The reason for the present request is to provide additional time to study the April 22 Office Action and take appropriate action in response thereto.

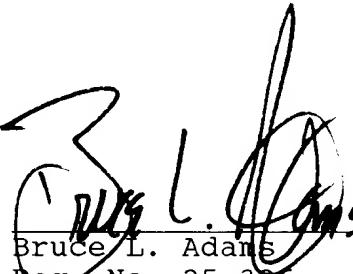
THE DEDICATION OF THE STAGE

A check in the amount \$400.00 is enclosed herewith to cover the required fee. Should the check prove insufficient for any reason, authorization is hereby given to charge any such insufficiency to our Deposit Account No. 01-0268.

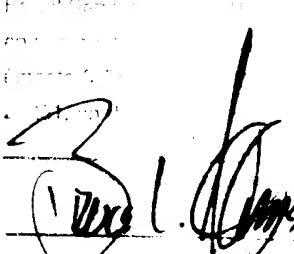
Respectfully submitted,

ADAMS & WILKS
Attorneys for Applicant

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SEPTEMBER 23, 2002